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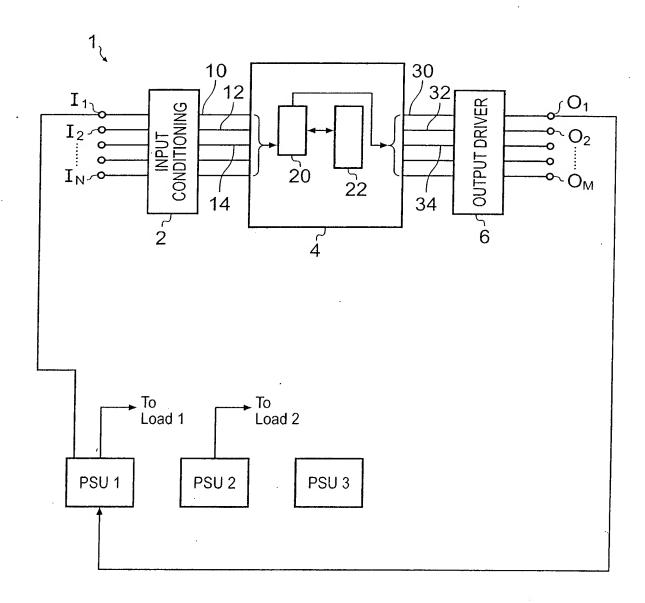


FIG. 1



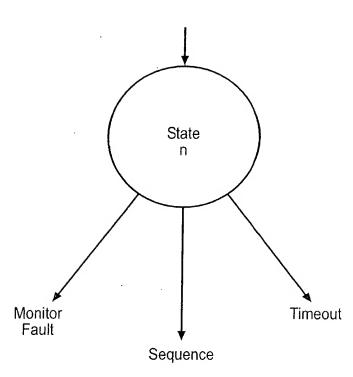
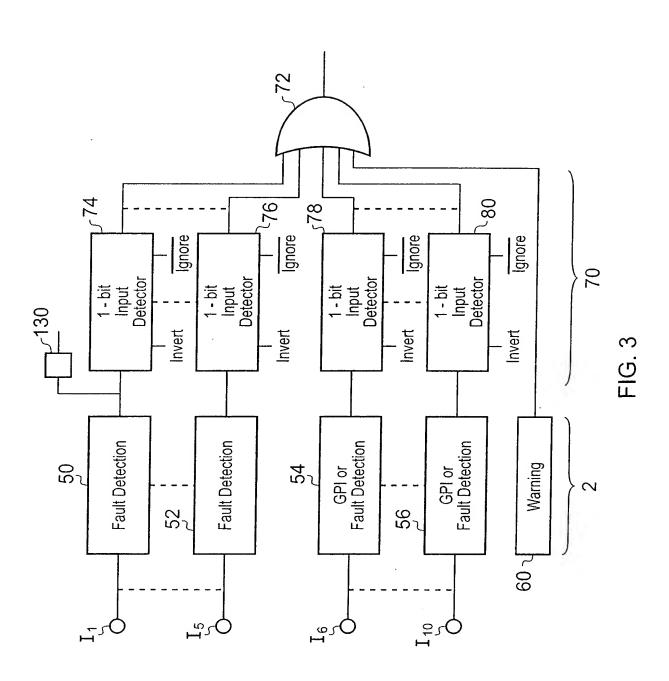


FIG. 2

PDO Outputs	IDLE1	IDLE2	EN3V3	EN2V5	DIS3V3	DIS2V5	PWRGD	FSEL1	FSEL2
PDO1 = 3V3ON	0	0	1	1	0	1	1	1	1
PDO2 = 2V50N	0	0	0	1	0	0	1	1	1
PDO3 = Fault	0	0	0	0	1	1	0	1	1

FIG. 10



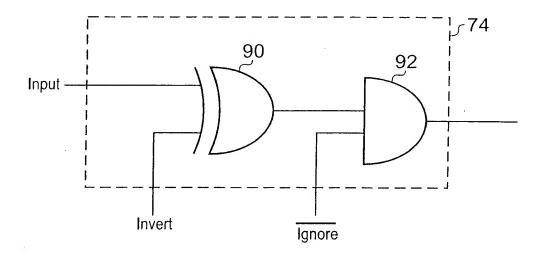
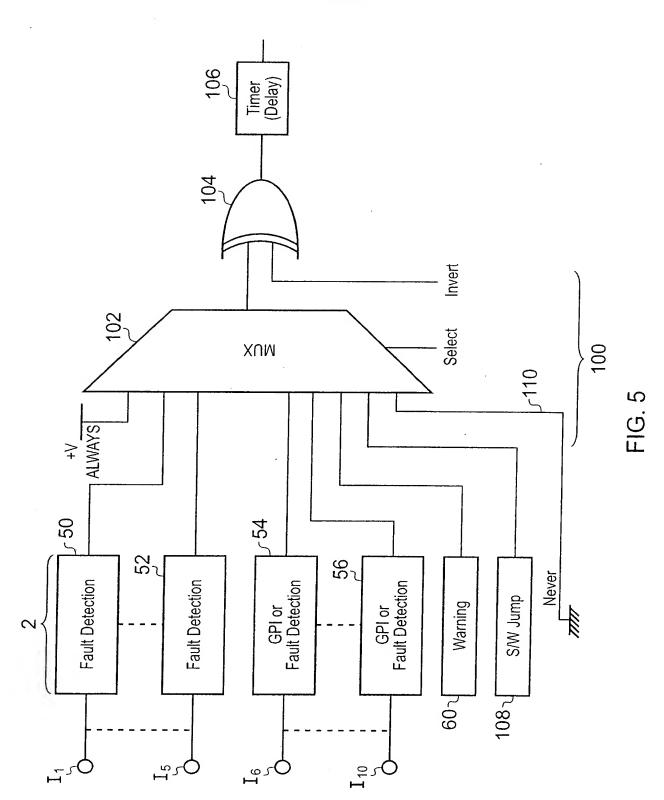


FIG. 4





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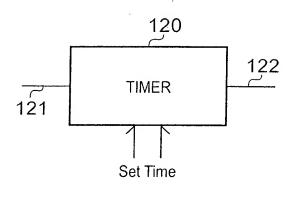


FIG. 6

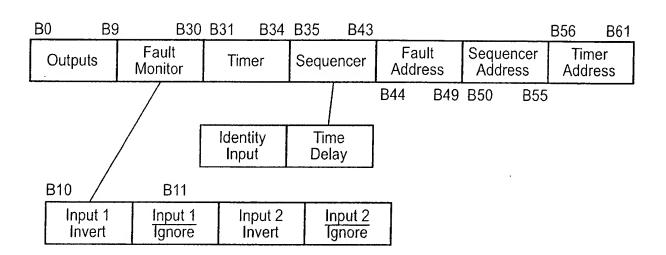


FIG. 7

Timeout Monitor	OW then IDLE2	JK then EN3V3	NK then If VP2 is NOT OK after EN2V5 10ms then goto State DIS3V3 then goto State IDLE1	IIGH then IDLE1	NK then If VP3 is NOT OK after PWRGD 20ms then goto State DIS2V5 then goto State FSEL2		IOT OK then If VP1 or VP2 is NOT OK then DIS2V5 then goto State FSEL2	IOT OK then If VP1 is NOT OK then DIS3V3	
End of Step	If VX1 is LOW then goto State IDLE2	If VP1 is OK then goto State EN3V3	If VP2 is OK then goto State EN2V5	If VX1 is HIGH then goto State IDLE1	If VP3 is OK then goto State PWRGD	If VX1 is HIGH then goto State DIS3V3	If VP3 is NOT OK then goto State DIS2V5	If VP2 is NOT OK then goto State DIS3V3	If VX1 is HIGH then
State	IDLE1	IDLE2	EN3V3	DIS3V3	EN2V5	DIS2V5	FSEL1	FSEL2	PWRGD

FIG. 8

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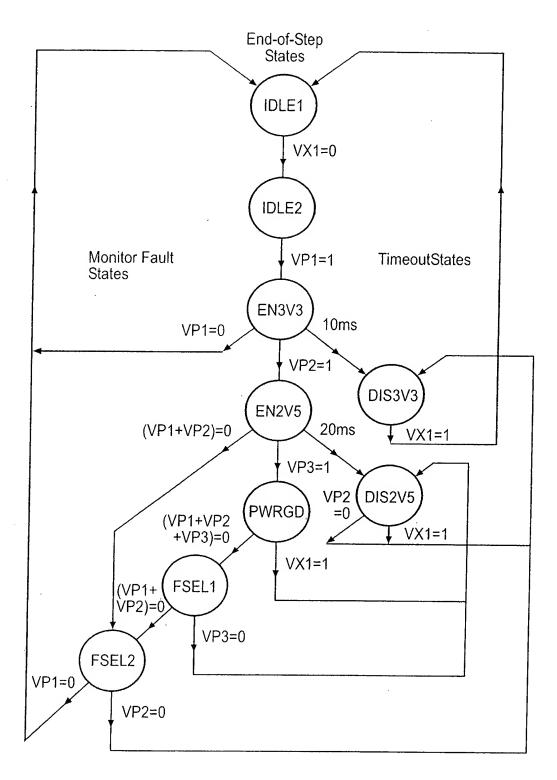


FIG. 9